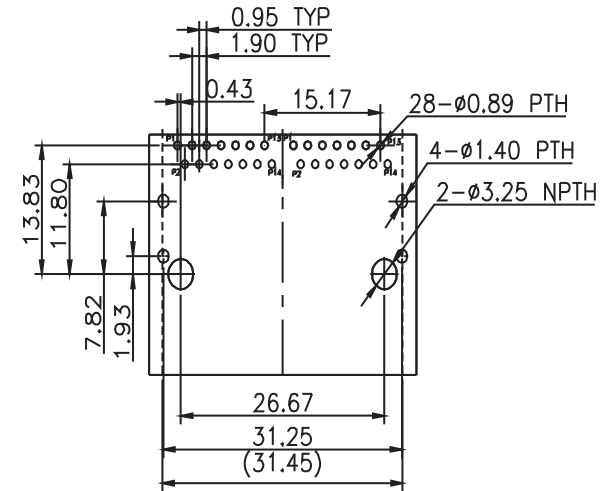
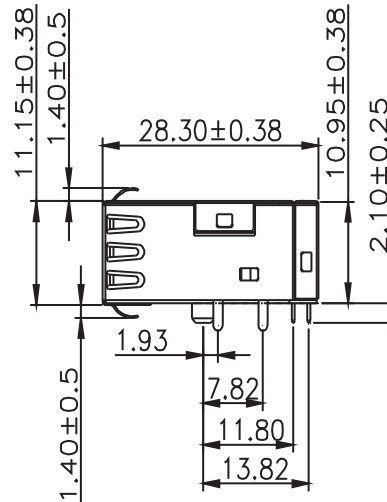
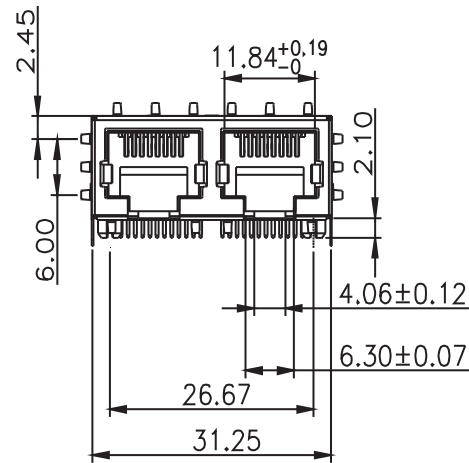
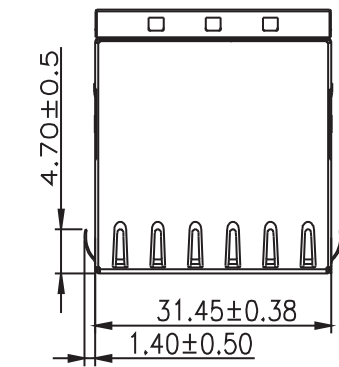




Recommended PCB Layout

HOW TO ORDER:

A 7 0 1 3 - 7 7 4 - 0 2 0 - X 0 - 0 1 - T 8 P - Z

NUMBER OF POSITIONS:
7 -14 Position (POE)

NUMBER OF CONTACTS:
7 -14 Position (POE)

KEYING OPTIONS:
0 - Without Key

PLATING OVERALL:

0 - FULL Au
1 - 10u" Au
2 - 15u" Au
3 - 30u" Au

SELECTIVE PLATING:

6 - Au CONTACT AREA/TIN TAIL
7 - 10u" Au CONTACT AREA/TIN TAIL
8 - 15u" Au CONTACT AREA/TIN TAIL
9 - 30u" Au CONTACT AREA/TIN TAIL
A - 50u" Au CONTACT AREA/TIN TAIL

**RoHS
COMPLIANT**

W/ TRANSFORMER:

T8P - Type 8P Transformer

MATERIAL:

CONTACT MATERIAL : PHOSPHOR BRONZE
INSULATOR: PA66 + 30 % Glass Filled (UL94V-0)
OPERATING TEMP: - 40° C to 85° C
STORAGE TEMP: -40° C to 85° C
CONTACT PLATING: Gold Plating over Nickel

SHIELD:

4 - Shielded with Spring
Tabs on Top, Sides and Bottom

Rev		Drawn	Checked	Approved	Date
0	Issued	B.S.	S.M.	S.M.	07/09/14

SMP TECHNOLOGY, INC.

RJ45 Jack, Low Profile, Gigabit Ethernet, Without LED, POESW

TOL. DEC. .X +/- 0.30 .XX +/- 0.25 .XXX +/-0.15 ANGLE +/- UNIT: mm

P/N: A7013-774-020-X0-01-T8P-Z

Pg: 1 of 2

