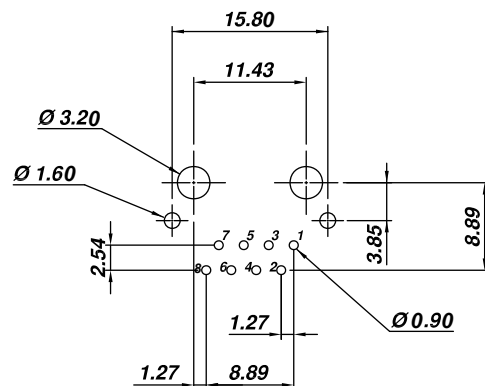
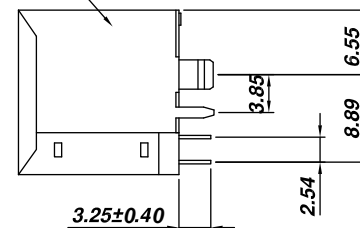
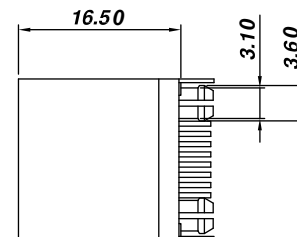


**SHIELDED**



**PCB LAYOUT**  
(Top View)



**HOW TO ORDER:**

A 3 0 0 2 - 3 4 1 - 0 1 0 - X 0 1 - Z

**NUMBER OF POSITIONS**

3 - 8 Pos

**NUMBER OF CONTACTS:**

4 - 8 CONTACTS

**SHIELD OPTION:**

1 - SHIELDED

**KEYING OPTION:**

0 - NON KEYED

**NUMBER OF PORTS:**

01 - SINGLE PORT

**PLATING OVERALL:**

0 - FULL Au FLASH  
1 - 10u" Au  
2 - 30u" Au  
3 - 50u" Au

**SELECTIVE PLATING:**

5 - 50u" Au CONTACT AREA/TIN TAIL  
6 - Au FLASH CONTACT AREA/TIN TAIL  
7 - 10u" Au CONTACT AREA/TIN TAIL  
8 - 15u" Au CONTACT AREA/TIN TAIL  
9 - 30u" Au CONTACT AREA/TIN TAIL

**RoHS**  
**COMPLIANT**

**ELECTRICAL:**

CURRENT RATING: 1.5A Max.  
VOLTAGE RATING: 120V AC  
CONTACT RESISTANCE: 20milli-Ohms Max.  
TEMPERATURE RANGE: -25°C TO +90°C  
INSULATION RESISTANCE: 500M-Ohms  
DIELECTRIC VOLTAGE: 1,000 Vrms 60 Sec. Min.

**MATERIAL:**

SPRING WIRE: 0.35mm Thickness Phosphor Bronze  
Gold Plating Over 50u" Nickel  
INSULATOR: 150u" Tin-Lead over 50u" Nickel,  
High Temp Glass Filled Polyester 94V-0

**ENVIRONMENTAL:**

Mates with Modular Plug Conforming to FCC Part 68, Subpart F

| Rev                                                                         |                             | Drawn | Checked | Approved | Date     | <b>SMP TECHNOLOGY, INC.</b>                         |
|-----------------------------------------------------------------------------|-----------------------------|-------|---------|----------|----------|-----------------------------------------------------|
| 0                                                                           | Issued                      | D.H.  | S.M.    | S.M.     | 05/28/04 |                                                     |
| A                                                                           | Revised for RoHS Compliance | B.S.  | S.M.    | S.M.     | 3/6/07   |                                                     |
| B                                                                           | Revised PCB Layout          | B.S.  | S.M.    | S.M.     | 10/22/09 |                                                     |
|                                                                             |                             |       |         |          |          | RJ45 Top Entry, Single Port, Shielded, Back 3.85 mm |
| TOL. DEC. .X +/- 0.25 .XX +/- 0.13 .XXX +/- 0.05 ANGLE +0.45 -0.15 UNIT: mm |                             |       |         |          |          | P/N: A3002 - 341 - 010 - x01 - Z Pg: 1              |